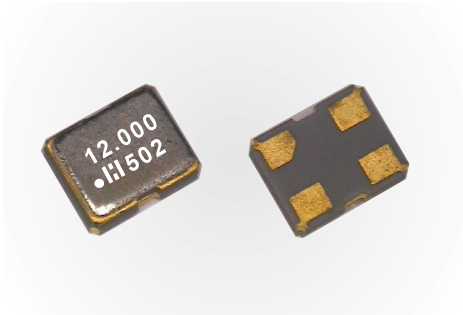


• D1SX Series 2.0*1.6 OSC



FEATURES

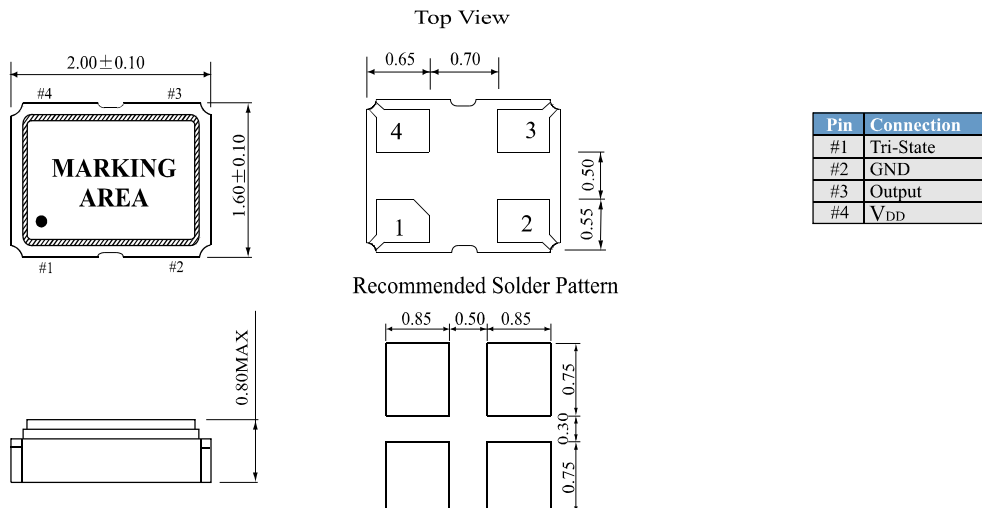
- 2.0X1.6X0.8 Miniature Package
- Tri-State Enable/Disable
- TTL/CMOS compatible
- Home security devices, power saving application, and low frequency specification circuit design looking for a smaller package
- 3.3V, 2.8V(2.5V), 1.8V option

Electrical Specifications

Parameter	Condition	DISX			
Frequency Range	F ₀	1~60MHz			
Frequency Stability*	All Condition*	± 25ppm, ± 50ppm, ± 100ppm			
Operating Temperature Range	T _{OPR}	-20°C~+70°C(-40°C~+85°C option)			
Storage Temperature Range	T _{STG}	-55°C~+125°C			
Power supply Voltage	V _{DD}	3.3V+/-10%	2.8V+/-10%	1.8V+/-10%	
Supply Current	I _{DD}	1.000MHz to 9.999MHz	8mA Max	7mA Max	6mA Max
		10.000MHz to 34.999MHz	10mA Max	8mA Max	7mA Max
		35.000MHz to 49.999MHz	25mA Max	20mA Max	15mA Max
		50.000MHz to 54.000MHz	35mA Max	30mA Max	25mA Max
Output Symmetry	Sym	At 1/2V _{DD}	40/60%(45/55% Option)		
Rise time	T _r	10%V _{DD} ~90%V _{DD}	5 nS Max	6 nS Max	7 nS Max
Fall Time	T _f	90%V _{DD} ~10%V _{DD}	5 nS Max	6 nS Max	7 nS Max
Output Voltage	V _{OH}		90% V _{DD} Min		
	V _{OL}		10% V _{DD} Max		
Output Load	HCMOS Load		15pF Typ.		
Start Time	T _s		10mS Max		
Stand-by Function			Yes		
Aging(First Year)		25°C ± 3°C	± 2ppm Max		
Pin 1,tri-state function			Pin 1=H or open....Output active at pin 3		
			Pin 1=L.....high impedance at pin 3		

*Include: 25°C tolerance, operating temperature range, input voltage change, aging, load change, shock and vibration

Mechanical Dimensions(mm)



**Note: 0.01uF bypass capacitor should be placed between V_{DD}(Pin4) and GND(Pin2) to Minimize power supply line noise

SPXO